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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Alexander J. Egli

Attorney Docket No.: GENSP103R1

Application No.: 10/720,001

Examiner: Unassigned

Filed: November 20, 2003

Group: 2673

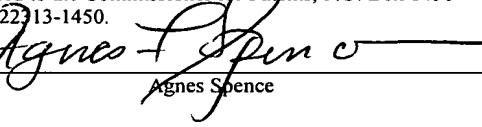
Title: **METHOD AND SYSTEM FOR
DISPLAYING AN ANALOG IMAGE BY A
DIGITAL DISPLAY DEVICE (as amended)**

Confirmation No. 3976

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on July 22, 2004 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed:


Agnes Spence

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. GENSP103R1).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP


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Form 1449 (Modified)
**Information Disclosure
Statement By Applicant**

(Use Several Sheets if Necessary)

 Atty Docket No.
GENSP103R1
Applicant:
Alexander Eglit
Filing Date
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	4,432,009	02/14/84	Reitmeier et al.			
	B	4,462,024	07/24/84	Strolle			
	C	4,720,745	01/19/88	01/19/88			
	D	4,952,125	08/28/90	Nagai			
	E	5,027,212	06/25/91	Marlton et al.			
	F	5,029,017	07/02/91	Abe et al.			
	G	5,227,882	07/13/93	Kato			
	H	5,276,515	01/04/94	Katsumata et al.			
	I	5,335,295	08/02/94	Ferracini et al.			
	J	5,410,357	04/25/95	Rieger et al.			
	K	5,469,223	11/21/95	Kimura			
	M	5,534,934	07/09/96	Katsumata			
	L	5,574,572	11/12/96	Malinowski			
	N	5,587,742	12/24/96	Hau et al.			
	O	5,594,467	01/14/97	Marlton et al.			
	P	5,600,347	02/04/97	Thompson et al.			
	Q	5,646,696	07/08/97	Sprague			
	R	5,703,618	12/30/97	Eglit			
	S	5,710,573	01/20/98	Hung et al.			
	T	5,719,633	02/17/98	Nishio et al.			
	U	5,739,808	04/14/98	Suga et al.			
	V	5,781,241	07/14/98	Donovan			
	W	5,790,096	08/04/98	Hill Jr.			
	X	5,838,381	11/17/98	Kasahara			
	Y	5,874,937	02/23/99	Kesatoshi			
	Z	5,909,205	06/01/99	Furuhashi et al.			
	A1	5,914,753	06/22/99	Donovan			
	B1	5,926,174	07/20/99	Shibamiya			
	C1	5,940,061	08/17/99	Sato			
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.


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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	D1	5,953,074	09/14/99	Reddy			
	E1	6,002,446	12/14/99	Eglit			
	F1	6,002,810	12/14/99	Wakisawa et al.			
	G1	6,037,925	03/14/00	Kim et al.			
	H1	6,067,071	12/23/00	Kotha et al.			
	I1	6,078,317	06/20/00	Sawada			
	J1	6,078,361	06/20/00	Reddy			
	K1	6,115,020	09/05/00	Taguchi et al.			
	L1	6,121,947	09/19/00	Furuhashi et al.			
	M1	6,195,079	02/27/01	Reddy			
	N1	6,215,467	04/10/01	Suga et al.			
	O1	6,219,023	04/17/01	Kim			
	P1	6,310,602	10/30/01	Kasai			
	Q1	6,310,618	10/30/01	Zhang			

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	R1	0479508	08.01.97	EPO				
	S1	502600	09.09.92	EPO				
	T1	01-169492	04.07.89	JPO			X	
	U1	02-135880	24.05.90	JPO			X	
	V1	02-302173	14.12.90	JPO			X	
	W1	02-033271	02.02.90	JPO			X	
	X1	04-138494	12.05.92	JPO			X	
	Y1	05-083534	02.04.93	JPO			X	
	Z1	05-150219	18.06.93	JPO			X	
Examiner				Date Considered				

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Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	A2	06-030247	04.02.94	JPO			X	
	B2	06-205256	22.07.94	JPO			X	
	C2	06/274611	30.09.94	JPO			X	
	D2	07-135592	23.05.95	JPO			X	
	E2	09-247574	19.09.97	JPO			X	
	F2	10-161635	19.06.98	JPO			X	
	G2	61-103369	21.05.86	JPO			X	
	H2	62-146066	30.06.87	JPO			X	
	I2	63-008983	14.01.88	JPO			X	
	J2	63-188276	03.08.88	JPO			X	

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	K2	“Combined Digital Zooming and Digital Effects System Utilizing CCD Signal Characteristics”, IEEE Transactions on Consumer Electronics, Vol. 39, No. 3, August 1993, pp. 398-406, Palk et al.
	L2	“Development of 13.3 In. Super TFT-LCD Monitor, SID International Symposium, Digest of Technical Papers, pp. 414-417, Kasai et al., May 1996
	M2	“Fast Prototyping of Video Interface Controller for AML CD System”, Proceedings of the 4th Asian Symposium on Information Display, pp. 195 - 198, Cheng et al. 02/13/1997
	N2	“Hardware Accelerators for Bitonal Image Processing” Digital Technical Journal, Vol. 3 No. 4, Fall 1991, pp. 1-36, Payson et al., Fall 1991
Examiner		Date Considered

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Information Disclosure Statement By Applicant		Applicant: Alexander Eglit	Group 2673
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O2	“High Speed Image Scaling for Integrated Document Management”, Second ACM-SIGOA Conference on Office Information Systems, Vol. 5, Nos. 1-2, pp. 36-45, Tabata et al., 6/25/1984
	P2	“High-Speed Rotation of Digital Images by Raster Scanning and Table-Lookup Operations”, Systems and Computers in Japan, Vol. 17, No. 11, pp. 76-87, Tabata et al., 1986
	Q2	“Low Cost Video Scaler and Gray Scale Integrator”, IEEE Transactions on Biomedical Engineering, Vol. 41, No. 7, pp. 698-703, Pietras & Bolanowski 7/1/1994
	R2	D’Alto et al publication: “A Highly Integrated Scanning Rate Converter for IQTV, 1994 IEEE - 6/27/1994.
	S2	Ferraro: Programmer’s Guide to the EGA and VGA Cards, 1988 p. 32, D’Alto V., et al.
	T2	Katsumata Article: “Development of Picture Converting System Applying an NTSC Signal to a Wide Aspect Display” K. Katsumata, et al., IEEE Trans. on CE; Vol. 38 No. 3, pp. 303-312, Aug. 1992.
Examiner		Date Considered

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